UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/608,232	06/30/2003	Jae-Yong Park	053785-5133	1756

9629 7590 04/04/2005

MORGAN LEWIS & BOCKIUS LLP
1111 PENNSYLVANIA AVENUE NW
WASHINGTON, DC 20004

EXAMINER	
RIELLEY, ELIZABETH A	

ART UNIT	PAPER NUMBER
2879	

DATE MAILED: 04/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/608,232	PARK ET AL.	
	Examiner	Art Unit	
	Elizabeth A. Rielley	2879	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *30 June 2003*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-34* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-34* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *30 June 2003* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☒ All b)☐ Some * c)☐ None of:

1.☒ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date *6/30/03&10/12/04*.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____.
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Priority

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

Drawings

2. The drawings are objected to as failing to comply with 37 CFR 1.84(p)(5) because they include the following reference character(s) not mentioned in the description: 57 in figure 3. Corrected drawing sheets in compliance with 37 CFR 1.121(d), or amendment to the specification to add the reference character(s) in the description in compliance with 37 CFR 1.121(b) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a

patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1, 2,4, 6-10, 12, 13, 15, 17-19, 21-28, and 30-33 are rejected under 35 U.S.C. 102(e) as being anticipated by Yoneda et al (US 20010026127).

5. In regard to claim 1, Yoneda et al ('127) teaches an organic electroluminescent display device, (figure 2) comprising: first and second substrates bonded together (2, 21; paragraphs 34, 38 and 20; the Examiner notes that the "bonded together" limitation is not in the figures, see paragraph 20), the first and second substrates having a plurality of pixel regions (abstract); a plurality of driving elements (3, 4, 5, 6, 7, 8; paragraphs 34-35) on an inner surface of the first substrate (2) within each of the plurality of pixel regions; a plurality of connection electrodes contacting the driving elements (9; paragraph 36); a black matrix (23) on an inner surface of the second substrate (21) at a boundary of each of the plurality of pixel regions (paragraph 39); a color filter layer including red (24R), green (24G), and blue (24B) color filters on the inner surface of the second substrate (21), each of the red, green, and blue color filters corresponding to each of the plurality of pixel regions (paragraph 39); a first electrode on the black matrix and the color filter layer (17); an organic electroluminescent layer (14; paragraph 36) on the first electrode; and at least one second electrode (12) on the organic electroluminescent layer, wherein the at least one second electrode contacts the connection electrodes (9; paragraph 36).

6. In regard to claims 2, 13, 19, and 28, Yoneda et al ('127) teaches the organic electroluminescent layer (14) includes an organic material emitting white light (paragraph 42).

7. In regard to claims 4, 15, 21, and 30, Yoneda et al ('127) teaches a plurality of sidewalls (18) on the first electrode (17) corresponding to the black matrix (23; paragraph 37).

8. In regard to claims 6 and 22, Yoneda et al ('127) teaches the first electrode includes one of an indium-tin-oxide (ITO) or an indium-zinc-oxide (IZO) (paragraph 36).

9. In regard to claims 7 and 24, Yoneda et al ('127) teaches at least one second electrode includes at least one of aluminum (Al), calcium (Ca), magnesium (Mg), and lithium (Li) (paragraph 36).

10. In regard to claims 8, 17, 23, and 31, Yoneda et al ('127) teaches the organic electroluminescent layer includes a hole-transporting layer (15) and an electron-transporting layer (13; paragraph 36).

11. In regard to claims 9 and 25, Yoneda et al ('127) teaches at least one second electrode (12) includes a plurality of the second electrodes (see figure 2).

12. In regard to claims 10 and 26, Yoneda et al ('127) teaches each of the plurality of second electrodes (12) contact each of the connection electrodes (9; paragraph 36).

13. In regard to claim 12, Yoneda et al ('127) teaches a method of fabricating an organic electroluminescent display device, comprising: forming a plurality of driving elements (3-8; figure 2; paragraphs 34-35) on a first substrate (2) having a plurality of pixel regions (1); forming a connection pattern contacting the driving elements (9); forming black matrix (23) on a second substrate (21) having the plurality of pixel regions (10, the black matrix being formed along a boundary of each of the plurality of pixel regions (paragraph 39); forming a color filter layer including red, green, and blue color filters on

a second substrate (22), each of the red, green, and blue color filters corresponding to each of the plurality of pixel regions (paragraph 39); forming a first electrode (17) on the black matrix (23; paragraph 20) and the color filter layer (22); forming an organic electroluminescent layer (14) on the first electrode (17); forming at least one second electrode (12) on the organic electroluminescent layer; and bonding the first and second substrates together (paragraph 20), wherein the connection pattern (9) contacts the at least one second electrode (12).

14. In regard to claim 18, Yoneda et al ('127) teaches an organic electroluminescent display device (figure 2), comprising: first and second substrates bonded together (2, 21; paragraphs 34, 38 and 20; the Examiner notes that the "bonded together" limitation is not in the figures, see paragraph 20), the first and second substrates having a plurality of pixel regions (1; paragraph 39); a plurality of driving elements (3, 4, 5, 6, 7, 8; paragraphs 34-35) on an inner surface of the first substrate (2) within each of the plurality of pixel regions; a first electrode connected to the driving elements (9; paragraph 36); an organic electroluminescent layer on the first electrode (14); at least one second electrode on the organic electroluminescent layer (17); a black matrix (23) on an inner surface of the second substrate (20) along a boundary of each of the plurality of pixel regions (paragraph 39); and a color filter layer (22R,G,B) including red, green, and blue color filters on the inner surface of the second substrate (21), each of the red, green, and blue color filters corresponding to each of the plurality of pixel regions (paragraph 39).

15. In regard to claim 27, Yoneda et al ('127) teaches a method of fabricating an organic electroluminescent display device (figure 2), comprising: forming a plurality of driving elements (3-8; paragraphs 34-35) on a first substrate (2) having a plurality of pixel regions (1); forming a first electrode (12) connected to the driving elements (via 9; paragraph 35); forming an organic electroluminescent layer (14) on the first electrode (12); forming a second electrode on the organic electroluminescent layer (17);

forming a black matrix (23) on a second substrate (21) having the plurality of pixel regions (1), the black matrix being formed along a boundary of each of the plurality of pixel regions (39); forming a color filter layer including red, green, and blue color filters (22) on the second substrate (21), each of the red, green, and blue color filters corresponding to each of the plurality of pixel regions (paragraph 39); and bonding the first and second substrates together (paragraph 20), wherein the color filter layer (22) faces the second electrode (17).

16. In regard to claim 32, Yoneda et al ('127) teaches an organic electroluminescent display device (figure 2), comprising: a plurality of driving elements (3, 4, 5, 6, 7, 8; paragraphs 34-35) on an inner surface of a first substrate (2) within each of a plurality of pixel regions (paragraph 39); a plurality of connection electrodes (9) contacting the driving elements; a black matrix (23) on an inner surface of the second substrate (21) at a boundary of each of the plurality of pixel regions (paragraph 39); a color filter layer (22R,G,B)including red, green, and blue color filters on the inner surface of the second substrate (21), each of the red, green, and blue color filters corresponding to each of the plurality of pixel regions (paragraph 39); a first electrode (17) on the black matrix and the color filter layer; an organic electroluminescent layer (14) on the first electrode; and a plurality of second electrodes (12) on the organic electroluminescent layer, wherein each of the second electrodes contact one of the connection electrodes (9).

17. In regard to claim 33, Yoneda et al ('127) teaches an organic electroluminescent display device (figure 2; paragraphs 33 to 39), comprising: a plurality of driving elements (3, 4, 5, 6, 7, 8; paragraphs 34-35) on an inner surface of a first substrate (2) within each of a plurality of pixel regions (paragraph 39); a plurality of connection electrodes (9) contacting the driving elements; a black matrix (23) on an inner surface of the second substrate (21) at a boundary of each of the plurality of pixel regions (paragraph 39);

a color filter layer (22) including red, green, and blue color filters on the inner surface of the second substrate (21), each of the red, green, and blue color filters corresponding to each of the plurality of pixel regions (paragraph 39); a first electrode on the black matrix (17) and the color filter layer (22); a plurality of sidewalls (18) on the first electrode corresponding to the black matrix; a plurality of organic electroluminescent layer segments (13-16) on the first electrode (17) between the sidewalls (18), each of the organic electroluminescent segments include a hole-transporting layer (15) and an electron-transporting layer (13); and a plurality of second electrodes (12) each on one of the organic electroluminescent layer segments (13), wherein each of the second electrodes contact one of the connection electrodes (9).

Claim Rejections - 35 USC § 103

18. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

19. Claims 3, 14, 20, and 29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yoneda et al (US 20010026127) in view of Shirasaki et al (US 5834894).

20. In regard to claims 3, 14, and 20, Yoneda et al ('127) describes all the limitations set forth, as described above, except the organic electroluminescent layer includes an organic material emitting red, green, and blue colored light corresponding to each of the red, green, and blue color filters. Shirasaki et al ('894) teaches the organic electroluminescent layer (65; figure 13; column 9 lines 5-6) includes an

organic material emitting red, green, and blue colored light (65R,G,B) corresponding to each of the red, green, and blue color filters (63R,G,B; column 9 lines 34-42) in order to produce a more vibrant light from each pixel. Hence, it would have been obvious at the time of the invention to one of ordinary skill in the art to combine the organic EL device of Yoneda et al ('127) with the corresponding el and filter layers of Shirasaki et al ("894). Motivation would be to produce a more vibrant light from each pixel.

21. Claims 5, 16, and 34 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yoneda et al (US 20010026127) in view of Okamoto et al (US 5543685).

22. In regard to claims 5 and 16, Yoneda et al ('127) describes all the limitations set forth, as described above, except a planarization layer between the first electrode and the color filter layer, the planarization layer includes a transparent insulating material. Okamoto et al ('685) teaches a planarization layer (3; figure 1; abstract) between the first electrode (7) and the color filter layer (R, G, B), the planarization layer includes a transparent insulating material (abstract) in order to protect the electrodes and the filters (abstract; column 3 lines 44-50 and column 4 lines 19-25). Hence, it would have been obvious at the time of the invention to one of ordinary skill in the art to combine the organic EL device of Yoneda et al ('127) with the planarization layer of Okamoto et al ('685). Motivation would be to protect the electrodes and the filters.

23. In regard to claim 34, Yoneda et al ('127) teaches an organic electroluminescent display device (figure 2), comprising: a plurality of driving elements (3, 4, 5, 6, 7, 8; paragraphs 34-35) on an inner surface of a first substrate within each of a plurality of pixel regions (paragraph 39); a plurality of first electrodes (9) contacting each of the driving elements; a black matrix (23) on an inner surface of the second substrate (21) at a boundary of each of the plurality of pixel regions (paragraph 39); a color filter

layer (22) including red, green, and blue color filters on the inner surface of the second substrate (21), each of the red, green, and blue color filters corresponding to each of the plurality of pixel regions (paragraph 39); a second electrode (37); and an organic electroluminescent layer (34) on the second electrode (37), wherein the organic electroluminescent layer contacts each of the first plurality of electrodes (via 37; paragraph 39). Yoneda et al ('127) fails to teach a planarization layer on the black matrix and the color filter layer and the second electrode located on the planarization layer. Okamoto et al ('685) teaches a planarization layer (3; figure 1; abstract), and an electrode located on the planarization layer (7; column 3 lines 44-50 and column 4 lines 19-25) in order to protect the electrodes and the filters (abstract). Hence, it would have been obvious at the time of the invention to one of ordinary skill in the art to combine the organic EL device of Yoneda et al ('127) with the planarization layer of Okamoto et al ('685). Motivation would be to protect the electrodes and the filters.

24. Claim 11 is rejected under 35 U.S.C. 103(a) as being unpatentable over Yoneda et al (US 20010026127) in view of Kanai et al (US 6121727).

25. Yoneda et al ('127) describes all the limitations set forth, as described above, except the second electrodes include a double-layered structure including lithium flourine and aluminum. Kanai et al ('727) teaches electrodes including a double-layered structure (4 and 5; figure 1; column 3 lines 20-25) including lithium flourine and aluminum (column 15 lines 59 - 61; column 12 lines 25-26) in order to prevent deterioration of the device (column 2 lines 6-21). Hence, it would have been obvious at the time of the invention to one of ordinary skill in the art to combine the organic EL device of Yoneda et al ('127) with the electrode structure of Kanai et al ('727). Motivation would be to prevent deterioration of the device.

Conclusion

26. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Eida et al (US 5869929) teaches the use of both filters and phosphor layers in an EL device. Terao et al (US 6133581) teaches the use specific electrode compositions for an EL device.

27. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Elizabeth A. Rielley whose telephone number is 571-272-2117. The examiner can normally be reached on Monday - Friday 7:30 - 4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimeshkumar Patel can be reached on 571-272-2457. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Elizabeth Rielley

Elizabeth Rielley

Examiner
Art Unit 2879

USPTO 3/30/05
Maricelí Santiago
AU 2879